



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Yasuhiko TAKEMURA

Serial No. 09/342,235

Filed: June 29, 1999

For: SEMICONDUCTOR DEVICE HAVING

AT LEAST FIRST AND SECOND

THIN FILM TRANSISTORS

Group Art Unit: 2826

Examiner: A. Sefer

**CERTIFICATE OF MAILING** 

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on June 21,

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Robert L Bilaud

## <u>RESPONSE</u>

Honorable Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

The Official Action mailed May 20, 2003 (Paper No. 27), and the Advisory Action mailed October 17, 2003 (Paper No. 20031010), have been received and their contents carefully noted. A Notice of Appeal was filed November 20, 2003, with a *Request for Three Month Extension of Time*. Filed concurrently herewith is a *Request for Five Month Extension of Time*, which extends the shortened statutory period for response to June 20, 2004. Accordingly, the Applicant respectfully submits that this response is being timely filed.

The Applicant notes with appreciation the consideration of the Information Disclosure Statements filed on June 29, 1999, October 25, 2000, October 31, 2000, June 4, 2001, November 30, 2001, March 7, 2002, May 7, 2002, November 8, 2002, and February 24, 2003.

Claims 6-11 and 13-25 are pending in the present, of which claims 6, 9-11, 13, 16 and 20 are independent. The Applicant notes with appreciation the allowance of

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claims 6-11 (page 3, Paper No. 27). For the reasons set forth in detail below, all claims are believed to be in condition for allowance. Favorable reconsideration is requested.

Paragraph 3 of the Official Action (page 2, <u>Id.</u>) rejects claims 13-25 as obvious based on the combination of U.S. Patent No. 5,051,570 to Tsujikawa et al. and U.S. Patent No. 5,302,966 to Stewart. The Applicant respectfully traverses the rejection because the Official Action has not made a *prima facie* case of obviousness.

As stated in MPEP §§ 2142-2143.01, to establish a prima facie case of obviousness, three basic criteria must be met. First, there must be some suggestion or motivation, either in the references themselves or in the knowledge generally available to one of ordinary skill in the art, to modify the reference or to combine reference teachings. Second, there must be a reasonable expectation of success. Finally, the prior art reference (or references when combined) must teach or suggest all the claim limitations. Obviousness can only be established by combining or modifying the teachings of the prior art to produce the claimed invention where there is some teaching, suggestion, or motivation to do so found either explicitly or implicitly in the references themselves or in the knowledge generally available to one of ordinary skill in the art. "The test for an implicit showing is what the combined teachings, knowledge of one of ordinary skill in the art, and the nature of the problem to be solved as a whole would have suggested to those of ordinary skill in the art." In re Kotzab, 217 F.3d 1365, 1370, 55 USPQ2d 1313, 1317 (Fed. Cir. 2000). See also In re Fine, 837 F.2d 1071, 5 USPQ2d 1596 (Fed. Cir. 1988); In re Jones, 958 F.2d 347, 21 USPQ2d 1941 (Fed. Cir. 1992).

The prior art, either alone or in combination, does not teach or suggest all the features of the independent claims. For example, independent claims 13, 16 and 20 recite a wiring for electrically connecting one of a pair of impurity regions of a first semiconductor island which is a part of an NTFT with a second gate electrode formed over a second semiconductor island which is a part of a PTFT. Tsujikawa and Stewart,

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either alone or in combination, do not teach or suggest at least the above-referenced features of the present invention.

Tsujikawa appears to teach active layers 114 and 115 in TFTs 103 and 104. However, Tsujikawa appears to be completely silent as to NTFTs and PTFTs. Also, Tsujikawa appears to be completely silent as to a wiring for connecting an impurity region of TFT 114 and a gate electrode of TFT 115. Further, the Official Action concedes that Tsujikawa does not teach a wiring connecting an impurity region of an island of an NTFT with a gate electrode of an island of a PTFT (page 3, Paper No. 27).

Stewart does not cure the deficiencies in Tsujikawa. The Official Action asserts that Stewart teaches "a wiring 332 connecting one of the impurity regions 226/330 of the NTFT first semiconductor island with the second gate electrode 206/334 of PTFT second semiconductor island" (Id.). Initially, it is noted that reference number 206 in Stewart refers to "a mask," so it is unclear what is meant by "second gate electrode 206/334." Further, it is noted that Figures 5a-5j, which include references 206 and 226, appear to be directed to a process for forming active matrix circuitry. Whereas, Figure 6, which includes references 330, 332 and 334, appears to be directed to an alternative embodiment which is independent from Figures 5a-5j. The specification of Stewart does not teach or suggest combining the device of Figures 5a-5j with the device of Figure 6. Therefore, it is unclear how the conductor 332 (allegedly the wiring of the present invention) of Figure 6 could be used with the N+-channel source/drain regions 226 or with a gate of a PTFT, presumably gate 216, of Figure 5. In other words, conductor 332 does not function with the device of Figures 5a-5j. In Figure 6, the conductor 332 appears to connect a drain region 330 with a gate 334. Figure 6 and the associated description in the specification appear to be silent as to NTFTs and PTFT.

Therefore, Tsujikawa and Stewart, either alone or in combination, do not teach or suggest a wiring for electrically connecting one of a pair of impurity regions of a first semiconductor island which is a part of an NTFT with a second gate electrode formed over a second semiconductor island which is a part of a PTFT. Since Tsujikawa and

Stewart do not teach or suggest all the claim limitations, a *prima facie* case of obviousness cannot be maintained.

Furthermore, there is no suggestion or motivation, either in the references themselves or in the knowledge generally available to one of ordinary skill in the art, to modify Tsujikawa and Stewart or to combine reference teachings to achieve the claimed invention.

The Official Action appears to assert that since Stewart discloses an NTFT and a PTFT in Figures 5a-5j and a conductor 332 in an alternate embodiment in Figure 6, that one of skill in the art at the time of the invention would have been motivated to first combine the device of Figures 5a-5j and the device of Figure 6, and then adapt the hypothetical combined device to Tsujikawa in order to form a wiring for electrically connecting one of a pair of impurity regions of a first semiconductor island which is a part of an NTFT with a second gate electrode formed over a second semiconductor island which is a part of a PTFT. There is no motivation to suggest such a modification of Stewart and subsequent combination with Tsujikawa. It is unclear why one of skill in the art would have found any reason or desire to alter the teachings of Tsujikawa in such a fashion and it is respectfully submitted that such teaching is insufficient to maintain a *prima facie* case of obviousness.

The burden of showing sufficient motivation to combine references lies with the Office. MPEP § 2142 states "The examiner bears the initial burden of factually supporting any *prima facie* conclusion of obviousness. If the examiner does not produce a *prima facie* case, the applicant is under no obligation to submit evidence of nonobviousness. ... The initial evaluation of *prima facie* obviousness thus relieves both the examiner and applicant from evaluating evidence beyond the prior art and the evidence in the specification as filed until the art has been shown to suggest the claimed invention."

It is respectfully submitted that the Official Action has failed to carry this burden. While the Official Action relies on various teachings of the cited prior art to disclose

aspects of the claimed invention and asserts that these aspects could be used together, it is submitted that the Official Action does not adequately set forth why one of skill in the art would combine the references to achieve the present invention. MPEP § 2142 further states: "The initial burden is on the examiner to provide some suggestion of the desirability of doing what the inventor has done. 'To support the conclusion that the claimed invention is directed to obvious subject matter, either the references must expressly or impliedly suggest the claimed invention or the examiner must present a convincing line of reasoning as to why the artisan would have found the claimed invention to have been obvious in light of the teachings of the references.' Ex parte Clapp, 227 USPQ 972, 973 (Bd. Pat. App. & Inter. 1985)."

For the reasons stated above, the Official Action has not formed a proper *prima* facie case of obviousness. Accordingly, reconsideration and withdrawal of the rejection under 35 U.S.C. § 103(a) are in order and respectfully requested.

Should the Examiner believe that anything further would be desirable to place this application in better condition for allowance, the Examiner is invited to contact the Applicant's undersigned at the telephone number listed below.

Respectfully submitted,

Robert L. Pilaud-Reg. No. 53,470

Robinson Intellectual Property Law Office, P.C. PMB 955 21010 Southbank Street Potomac Falls, Virginia 20165

(571) 434-6789



PTO/SB/21 (08-00)

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TRANSM	Filing Date		June 29, 1999			
FOR	First Name	d Inventor	Yasuhiko TAKEMURA			
	Group Art U	nit	2826			
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Total Number of Pages in This S	Attorney Do	cket Number	0756-1986			
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Firm or Individual name	Robert L. Pilaud, Reg. No. 53,470 Robinson Intellectual Property Law Office, P.C. PMB 955 21010 Southbank Street Potomac Falls, VA 20165					
Signature	ignature Dela Dela					
Date	June 21, 2004					
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Type or printed name	Robert L. P	ilaud	1			
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Effective 10/01/2003. Patent fees are subject to annual revision. ☐ Applicant Claims small entity status. See 37 CFR 1.27.

Signature

Complete if Known						
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Filing Date	June 29, 1999					
First Named Inventor	Yasuhiko TAKEMURA					
Examiner Name	A. Sefer					
Group Art Unit	2826					
Attorney Docket No.	0756-1986					

Telephone Date

June 21, 2004

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